

STICOMM Product Sheet

from Solidus Technologies, Inc.

Test Equipment

STICOMM Introduction

The STICOMM is a test instrument that provides extended tester resources, increased flexibility and scalability to the STI9000 test system. The main features contained in the STICOMM include multiplexing resources that provide increased parallel DUT testing capabilities, high voltage power supply resources, additional PMU resources, clock buffering, VDD buffering, LF generation, stimulus signal generation, RMS-DC conversion circuitry and current-to-voltage digital conversion circuitry. The STICOMM_01 instrument provides 12 pins of tester resources to four devices under test and the STICOMM_02 instrument provides similar multiplexing capabilities as The STICOMM_01 with additional test resources. The STICOMM_03 instrument provides 12 pins of tester resources to eight devices under test. The STI9000 test instruments are combined with STICOMM instruments to provide a range of serial and parallel device testing configurations with increased performance and flexibility.

STICOMM 01 Features

- Mux 12 pins of tester resources to 4 DUTs
- Clock Buffering
- VDD Buffering
- LF Generation
- 2 Channels of Stimulus Sine Generation
- 2 Channels of RMS-DC Conversion

STICOMM 01 Resources

STICOMM Modules	Stim Gen	RMS-DC	LF Gen	Digital Pins	DUT's
1	2	2	1	12	4
2	4	4	2	24	8
4	8	8	4	36	16

STICOMM 02 Features

- Mux 12 pins of tester resources to 4 DUTs
- 0 to 60V High Current Supplies
- High Voltage, High Current Pin Drivers
- Current to Voltage Digital Conversion
- Stimulus Signal Generation
- RMS-DC Conversion
- PMU Resources

STICOMM 02 Resources

STICOMM Modules	DPS	PMU	DAC Stim	Digital Pins	DUT's
1	4	8	2	12	4
2	8	16	4	24	8
4	16	32	8	36	16

STICOMM 03 Features

- Mux 12 pins of tester resources to 8 DUTs
- 0 to 60V High Current Supplies
- High Voltage, High Current Pin Drivers
- Current to Voltage Digital Conversion
- Stimulus Signal Generation
- RMS-DC Conversion
- PMU Resources

STICOMM 03 Resources

STICOMM Modules	DPS	PMU	DAC Stim	Digital Pins	DUT's
1	4	8	2	12	8
2	8	16	4	24	16
4	16	32	8	36	32



STI9000 Test System



STI9000



STICOMM 01



STICOMM 02



STICOMM 03

For more information, contact your Solidus Technologies, Inc. Representative

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STICOMM Specifications

Multiplexer

Instrument	Specification
STICOMM_01	12 pins to 4 DUTs
STICOMM_02	12 pins to 4 DUTs
STICOMM_03	12 pins to 8 DUTs

Stimulus Signal Generator

Instrument	Specification
STICOMM_01	Sine : DC to 100Hz
STICOMM_02	ARB: DC to 1kHz Gain Control Power-up Mute
STICOMM_03	ARB: DC to 1kHz Gain Control Power-up Mute

RMS-DC Conversion

Instrument	Specification
STICOMM_01	2-Channels
STICOMM_02	2-Channels Variable Gain Input Shunt AC Input Bandwidth Control
STICOMM_03	2-Channels Variable Gain Input Shunt AC Input Bandwidth Control

Parametric Measurement Unit : Force Voltage/Measure Current

Instrument	Specification
STICOMM_01	Re-Use of STI9000 PMU Resources
STICOMM_02	Additional 8 Programmable
STICOMM_03	Additional 8 Programmable

Parametric Measurement Unit : Force Current/Measure Voltage

Instrument	Specification
STICOMM_01	Re-Use of STI9000 PMU Resources
STICOMM_02	Additional 8 Programmable
STICOMM_03	Additional 8 Programmable

Device Power Supplies

Instrument	Specification
STICOMM_01	Re-Use of STI9000 DPS Resources
STICOMM_02	(2) 0 to 16V Range, (2) 0 to 60V Range
STICOMM_03	(2) 0 to 16V Range, (2) 0 to 60V Range

Pin Drivers

Instrument	Specification
STICOMM_01	Re-Use of STI9000 DPS Resources
STICOMM_02	High Voltage, High Current (600mA @ 45V)
STICOMM_03	High Voltage, High Current (600mA @ 45V)

DUT Interface Resources

STI9000 Pin Driver/Receiver Architecture

The STI9000 has 24 digital pin interface circuits.

Pins 0 to 7 are Output only:

Can be connected to the PMU force and sense bus.

Pins 0 through 3 may be connected to the AWG output.

Pins 8 to 11 are I/O:

User selectable or controlled by the Stimulus pattern.

Can be connected to the PMU force and sense bus, AWG output or the Digitizer input.

Pins 12 to 15 are Input only:

Can be connected to the PMU force and sense bus.

Pins 16 to 23 are Input only:

Streaming Digital Input pins and are used only when the Digitizer is capturing analog signals.

STI9000 Pin Resource Architecture

- Arbitrary Waveform Generator
- Digitizer
- Device Power Supplies
- Precision Measurement Unit
- Zero Crossing Detector
- Asynchronous Clock
- Edge Counters
- Stimulus Response Clock Control
- Digital Stimulus
- Digital Response
- Pin Driver VOH & VIH
- 8 Digital Output pins
- 4 Digital I/O Pins
- 4 Digital Input Pins
- 8 Digital Input Streaming Pins
- Master/ Slave Bus
- 8 Channel Digital/Analog Converter



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